### **PATENT**

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

MANFRED HORSTMANN ROLF STEPHAN KARSTEN WIECZOREK STEPHEN KRUEGEL

Serial No.: 09/847,622

Filed: May 2, 2001

For: FIELD EFFECT TRANSISTOR WITH

REDUCED GATE DELAY AND METHOD OF FABRICATING THE

SAME

Examiner: Unknown

Group Art Unit: 2812.

Att'y Docket: 2000.064200/DE0053

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# PRELIMINARY AMENDMENT

#### CERTIFICATE OF TRANSMISSION

I hereby certify that this correspondence is being facsimile transmitted to the Patents and Trademark Office, Washington D.C., 20231, on the date below.

Assistant Commissioner for Patents Washington, D.C. 20231

Sir:

Please substitute this Preliminary Amendment for the Preliminary Amendment mailed on

October 1, 2001. Please amend the above-referenced patent application as follows:

# IN THE CLAIMS:

Please amend claim 1 as follows:

Sub 7 A1

(Amended) A transistor, comprising:

a substrate:

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144. an active region defined in said substrate;

a gate insulation layer formed above said active region; and



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No. of Pages:*	6	Date:	October 11, 2001	
Re:	S/N 09/847,622	File:	2000.064200	
* w/o cov	ent For Review For Your		Tricase neply	☐ Please Handle
ORIGINAL: Will follow X Will not follow				
• Comm	ents:			

Per our telephone conversation, attached is the revised Preliminary Amendment.

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